

Electronic Patent Application Fee Transmittal

Application Number:	10717977
Filing Date:	20-Nov-2003
Title of Invention:	PARALLEL MULTI-LAYER PRINTED CIRCUIT BOARD HAVING IMPROVED INTERCONNECTION AND METHOD FOR MANUFACTURING THE SAME
First Named Inventor/Applicant Name:	Jee-Soo Mok
Filer:	Laura Cruz/Marivick Watson
Attorney Docket Number:	LEPA122042

Filed as Large Entity

Utility Filing Fees

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Utility Appl issue fee	1501	1	1400	1400
Publ. Fee- early, voluntary, or normal	1504	1	300	300

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension-of-Time:				
Miscellaneous:				
Total in USD (\$)				1700